

# LESSON PLAN

## SUB-ANLOG ELECTRONICS AND LINIER IC

### BRANCH-ETC

### SEM -4TH

Week	Lecture	Topic to be covered
1 <sup>st</sup> week	1 <sup>st</sup>	<b>UNIT-1: DIODE, TRANSISTOR AND CIRCUIT</b> Working Principle of diode and current Equation, Specifications and use of PN junction diode, Construction of diode
	2 <sup>nd</sup>	Breakdown of diode, , Working of diode, Characteristic of diode
	3 <sup>rd</sup>	Classification of rectifier, Half wave rectifier
	4 <sup>th</sup>	Full wave rectifier
2 <sup>nd</sup> week	1 <sup>st</sup>	Working principle of p-n-p and n-p-n transistor,
	2 <sup>nd</sup>	Different types of transistor connection (CB, CE and CC)& input and output characteristics of transistor in different connections.
	3 <sup>rd</sup>	Define ALPHA, BETA and GAMMA of transistors in various modes. Establish the Mathematical relationship between them.
	4 <sup>th</sup>	Load line (AC & DC) and determine the Q-point.
	5 <sup>th</sup>	Basic concept of Biasing, Types of Biasing,
3 <sup>rd</sup> week	1 <sup>st</sup>	h-parameter model of BJT,
	2 <sup>nd</sup>	Types of Coupling, working principle and use of R-C Coupled Amplifier & Frequency Responses
	3 <sup>rd</sup>	<b>Unit-2: AUDIO POWER AMPLIFIERS.</b> Classify Power Amplifier & Differentiate between Voltage and Power Amplifier.
	4 <sup>th</sup>	Working principle of Class A Power Amplifier
	5 <sup>th</sup>	Working principle of Class B Power Amplifier
4 <sup>th</sup> week	1 <sup>st</sup>	Working principle of Class C Power Amplifier
	2 <sup>nd</sup>	Working principle of Class D Power Amplifier
	3 <sup>rd</sup>	Working principle of Class AB Power Amplifier

Week	Lecture	Topic to be covered
5 <sup>th</sup> week	1 <sup>st</sup>	Working of Class B push pull amplifier
	2 <sup>nd</sup>	<b>Unit-3: FIELD EFFECT TRANSISTOR (FET).</b> FET & its classifications & Differentiate between JFET & BJT.
	3 <sup>rd</sup>	Construction, working principle of N channel JFET and P channel JFET
	4 <sup>th</sup>	characteristics of JEFT
	5 <sup>th</sup>	Explain JEFT as an amplifier,
6 <sup>th</sup> week	1 <sup>st</sup>	Parameters of JFET & Establish relation among JFET parameters
	2 <sup>nd</sup>	Construction & its classification MOSFET
	3 <sup>rd</sup>	Working principle of MOSFET
	4 <sup>th</sup>	Characteristics (Drain & Transfer) of MOSFET
	5 <sup>th</sup>	Explain the operation of CMOS,
7 <sup>th</sup> week	1 <sup>st</sup>	Explain the operation of VMOS,
	2 <sup>nd</sup>	Explain the operation of LD MOS,
	3 <sup>rd</sup>	<b>Unit-4: FEED BACK AMPLIFIER &amp; OSCILLATOR</b> Define & classify Feedback Amplifier, Types of feedback – negative & positive feedback.
	4 <sup>th</sup>	Types of negative feedback – voltage shunt, voltage series,
8 <sup>th</sup> week	1 <sup>st</sup>	Types of negative feedback – current shunt, current series.
	2 <sup>nd</sup>	Characteristics voltage gain, bandwidth , input Impedance output impedance, stability, noise , distortion in amplifiers.

	3 <sup>rd</sup>	Oscillator -block diagram of sine wave oscillator
	4 <sup>th</sup>	Types Requirement of oscillation- Barkhausen criterion

13 <sup>th</sup> week	1 <sup>st</sup>	Define the following electrical characteristics input offset voltage, input offset current, CMMR, Large signal voltage gain, Slew rate
	2 <sup>nd</sup>	Explain the Open Loop configuration of non-inverting Amplifier
	3 <sup>rd</sup>	Explain the Open Loop configuration of inverting Amplifier
	4 <sup>th</sup>	Voltage series feedback amplifier and derive the close loop Voltage gain
	5 <sup>th</sup>	Gain of series feedback circuits input resistance, and output resistance, bandwidth and total output offset voltage with feedback
14 <sup>th</sup> week	1 <sup>st</sup>	Voltage shunt feedback amplifier and derive the close loop, Voltage gain,
	2 <sup>nd</sup>	Gain of shunt feedback circuits and input resistance, and output resistance, bandwidth and total output offset voltage with feedback.
	3 <sup>rd</sup>	<b>Unit-7. APPLICATION OF OPERATIONAL AMPLIFIER, TIMER CIRCUITS&amp; IC voltage regulator</b> summing scaling and averaging of inverting and non-inverting amplifiers
	4 <sup>th</sup>	DC & AC Amplifies using OP-AMP. Integrator and differentiator using op-amp.
15 <sup>th</sup> Week	1 <sup>st</sup>	Active filter and describe the filter design of fast order low Pass Butterworth, Concept of Zero-Crossing Detector using Op-Amp
	2 <sup>nd</sup>	Block diagram and operation of IC 555 timer & IC 565 PLL& its applications.
	3 <sup>rd</sup>	Working of Current to voltage and Voltage to Frequency and Frequency to Voltage Convertor using Operational Amplifier
	4 <sup>th</sup>	Operation of power supply using 78XX and 79XX, LM 317 Series with their PIN configuration , Functional block diagram & Working of IC regulator LM 723 & LM 317
	5 <sup>th</sup>	Functional block diagram & Working of IC regulator LM 723 & LM 317